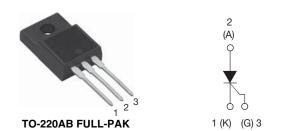
www.vishay.com

VS-25TTS..FPPbF Series, VS-25TTS..FP-M3 Series

Vishay Semiconductors

Thyristor High Voltage, Phase Control SCR, 25 A



PRODUCT SUMMARY		
Package	TO-220AB FP	
Diode variation	Single SCR	
I _{T(AV)}	16 A	
V _{DRM} /V _{RRM}	800 V, 1200 V	
V _{TM}	1.25 V	
I _{GT}	45 mA	
TJ	- 40 °C to 125 °C	

FEATURES

- · Designed and qualified for industrial level
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL E78996 pending
- 125 °C max. operating junction temperature
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

• Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding, and battery charge

DESCRIPTION

The VS-25TTS...FP... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS	
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	18	22	А	

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I _{T(AV)}	Sinusoidal waveform	16	٨	
I _{RMS}		25	A	
V _{RRM} /V _{DRM}		800/1200	V	
I _{TSM}		350	A	
V _T	16 A, T _J = 25 °C	1.25	V	
dV/dt		500	V/µs	
dl/dt		150	A/µs	
TJ		- 40 to 125	°C	

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA		
VS-25TTS08FPPbF, VS-25TTS08FP-M3	800	800	10		
VS-25TTS12FPPbF, VS-25TTS12FP-M3	1200	1200	- 10		

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL		TEST CONDITIONS		VALUES	
	STMBOL		TEST CONDITIONS	TYP.	MAX.	UNITS
Maximum average on-state current	I _{T(AV)}	$T_C = 51 \text{ °C}$, 180° conduction half sine wave		16	6	
Maximum RMS on-state current	I _{RMS}			25	5	А
Maximum peak, one-cycle,	1	10 ms sine p	oulse, rated V _{RRM} applied	30	0	A
non-repetitive surge current	I _{TSM}	10 ms sine p	ulse, no voltage reapplied	35	0	
Movimum 12t for fusing	l ² t	10 ms sine p	oulse, rated V _{RRM} applied	45	0	A ² s
Maximum I ² t for fusing	I-t	10 ms sine p	10 ms sine pulse, no voltage reapplied		0	Ars
Maximum I ² √t for fusing	l²√t	t = 0.1ms to 10 ms, no voltage reapplied		630	00	A²√s
Maximum on-state voltage drop	V _{TM}	16 A, T _J = 25 °C		1.2	25	V
On-state slope resistance	r _t	T - 125 °C		12	.0	mΩ
Threshold voltage	V _{T(TO)}	1j=125 C	T _J = 125 °C		0	V
Maximum reverse and direct locks as surrent	1 /1	T _J = 25 °C	V Deted V A/	0.	5	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	$T_J = 125 \text{ °C}$ $V_R = \text{Rated } V_{RRM}/V_{DRM}$		10)	1
Holding current	Ι _Η	Anode supply = 6 V, resistive load, initial I_T = 1 A, T _J = 25 °C		-	150	mA
Maximum latching current	١L	Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$		20	0	
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J max.$, linear to 80 %, $V_{DRM} = R_g - k = Open$	50	0	V/µs
Maximum rate of rise of turned-on current	dl/dt			15	0	A/µs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P _{GM}		8.0	w	
Maximum average gate power	P _{G(AV)}		2.0	~~	
Maximum peak positive gate current	+ I _{GM}		1.5	А	
Maximum peak negative gate voltage	- V _{GM}		10	V	
	I _{GT}	Anode supply = 6 V, resistive load, T_J = - 10 °C	60		
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, T_J = 25 °C	45	mA	
		Anode supply = 6 V, resistive load, T_J = 125 °C	20	1	
		Anode supply = 6 V, resistive load, T_J = - 10 °C	2.5		
Maximum required DC gate voltage to trigger	V_{GT}	Anode supply = 6 V, resistive load, T_J = 25 °C	2.0		
voltage to trigger		Anode supply = 6 V, resistive load, T_J = 125 °C	1.0	V	
Maximum DC gate voltage not to trigger	V _{GD}	T = 125 °C V Poted value	0.25		
Maximum DC gate current not to trigger	I _{GD}	$T_{\rm J} = 125 ^{\circ}\text{C}, V_{\rm DRM} = \text{Rated value} $		mA	

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9	
Typical reverse recovery time	t _{rr}	T 105 %C	4	μs
Typical turn-off time	tq	T _J = 125 °C	110	

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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	2.5	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf ⋅ in)
Mandala surday fara				25TTS08FP	
Marking device			Case style TO-220AB FULL-PAK (94/V0)	25TTS12FP	

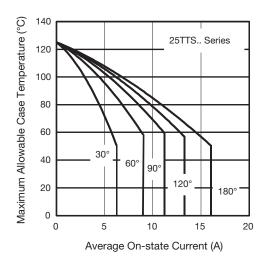
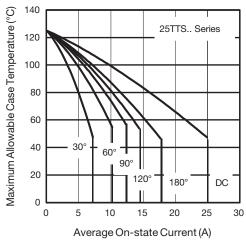
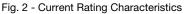


Fig. 1 - Current Rating Characteristics





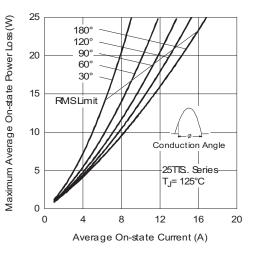


Fig. 3 - On-State Power Loss Characteristics

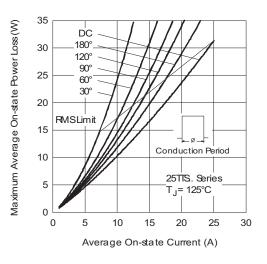


Fig. 4 - On-State Power Loss Characteristics

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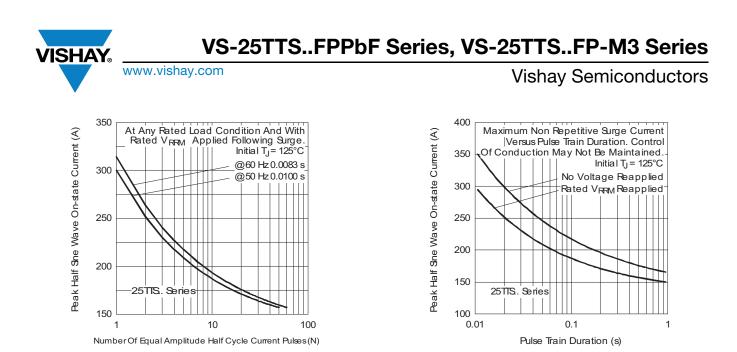




Fig. 6 - Maximum Non-Repetitive Surge Current

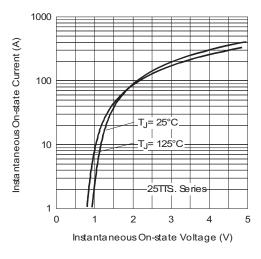
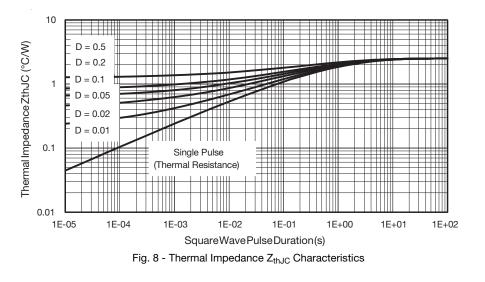
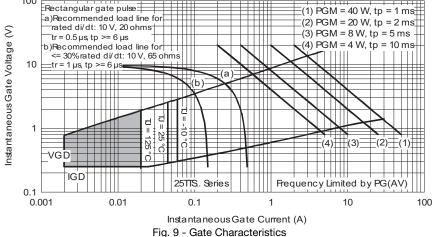


Fig. 7 - On-State Voltage Drop Characteristics

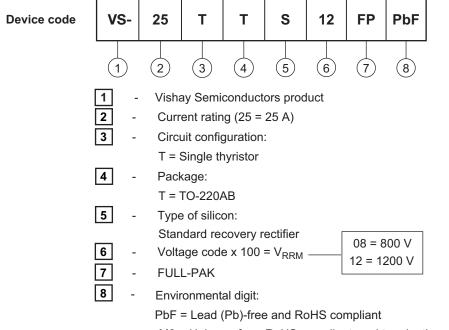


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ORDERING INFORMATION TABLE



-M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-25TTS08FPPbF	50	1000	Antistatic plastic tubes		
VS-25TTS08FP-M3	50	1000	Antistatic plastic tubes		
VS-25TTS12FPPbF	50	1000	Antistatic plastic tubes		
VS-25TTS12FP-M3	50	1000	Antistatic plastic tubes		

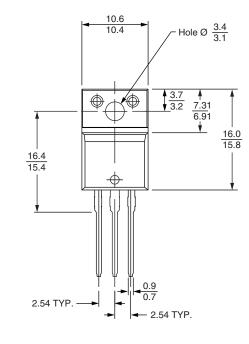
LINKS TO RELATED DOCUMENTS			
Dimensions		www.vishay.com/doc?95072	
Port marking information	TO-220FP PbF	www.vishay.com/doc?95069	
Part marking information	TO-220FP -M3	www.vishay.com/doc?95456	

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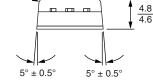
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DIMENSIONS in millimeters

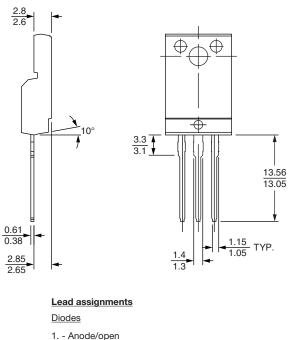


R 0.7 R 0.5 (2 places)





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2. - Cathode

3. - Anode

Conforms to JEDEC outline TO-220 FULL-PAK



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